

Manufacturer : Anshan Keifat Electronic Ceramic Technical Co.,Ltd. No:

Approval Sheet for Product Specification

Customer:

Product: Lead type High frequency Ceramic capacitors of Medium and high voltage

PART No.:

Mfr. P/N:

Date: 年 月 日

Manufacturer		Customer Confirm	
Prepared by	薛志豪	合 格 OK <input type="checkbox"/>	
		不合格 NG <input type="checkbox"/>	
Checked by	于金龙	Checked by	
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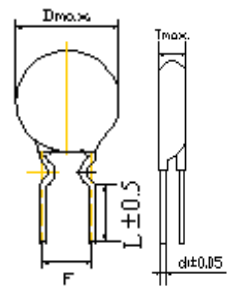
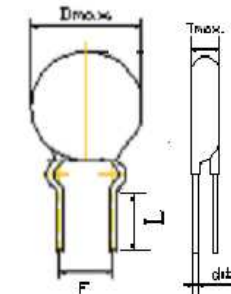


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<div>■ Features</div> <div><div>- High breakdown strength</div><div>- Operating temperature range -25~+125℃</div><div>- Coated with flame-retardant epoxy resin (conforming to UL94V-0 standard).</div><div>- DC1KV~6KV Rated Voltage item are available.</div><div>- Bulk or taping product can be available.</div></div> <div>■ Application</div> <div>Ideal for use as the ballast in back lighting inverters for liquid crystal display.</div> <div>Ideal for use on high frequency pulse circuits such as a horizontal resonance circuit for CTV and snubber circuits for switching power supplies.</div> <div>■ Part Number Designation</div> <div>CC81 - 1KV - SL - 220 J a 5 T</div> <div><div>①</div><div>②</div><div>③</div><div>④</div><div>⑤</div><div>⑥</div><div>⑦</div><div>⑧</div></div> <div>①Type</div> <table><tr><th>Code</th><th>Type Designation</th></tr><tr><td>CC81</td><td>Class I High-Voltage</td></tr></table> <div>②Rated Voltage</div> <table><tr><th>Code</th><th>Rated Vol. (DC)</th></tr><tr><td>1KV</td><td>1000V</td></tr><tr><td>3KV</td><td>3000V</td></tr></table> <div>③Temperature Characteristic</div> <table><tr><th>GB</th><th>EIA</th><th>Temp. range</th><th>Cap. Change</th></tr><tr><td>SL</td><td></td><td>+25~+85℃</td><td>+350~-1000ppm/℃</td></tr></table> <div>④Capacitance</div> <table><tr><th>Code</th><th>Capacitance</th></tr><tr><td>220</td><td>22 pF</td></tr><tr><td>101</td><td>100 pF</td></tr></table> <div>⑤Tolerance</div> <table><tr><th>Code</th><th>Tolerance</th></tr><tr><td>J</td><td>± 5%</td></tr><tr><td>K</td><td>± 10%</td></tr></table> <div>⑥Lead Shape</div> <table><tr><th>Code</th><th>Shape</th></tr><tr><td>a</td><td>In-kink</td></tr><tr><td>k</td><td>Out-kink</td></tr></table> <div>⑦Lead Space</div> <table><tr><th>Code</th><th>Lead Space</th></tr><tr><td>5</td><td>5.0±0.5mm</td></tr><tr><td>7.5</td><td>7.5±0.5mm</td></tr></table> <div>⑧Package</div> <table><tr><th>Code</th><th>Shape</th></tr><tr><td>T</td><td>Taping</td></tr></table>			Code	Type Designation	CC81	Class I High-Voltage	Code	Rated Vol. (DC)	1KV	1000V	3KV	3000V	GB	EIA	Temp. range	Cap. Change	SL		+25~+85℃	+350~-1000ppm/℃	Code	Capacitance	220	22 pF	101	100 pF	Code	Tolerance	J	± 5%	K	± 10%	Code	Shape	a	In-kink	k	Out-kink	Code	Lead Space	5	5.0±0.5mm	7.5	7.5±0.5mm	Code	Shape	T	Taping
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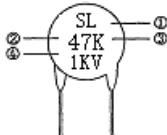


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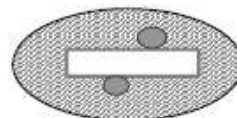
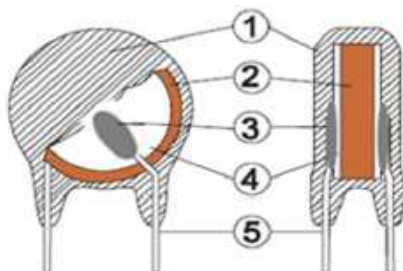
■ Appearance and Structure

CODE	CODE NO.	D _{max} (mm)	T _{max} (mm)	L (mm)	F (mm)	d (mm)	STYLE
	CC81-1KV-SL-020~101K a**	See specific specification				0.5	a 式 
	CC81-2KV-SL-020~221K a**	See specific specification				0.6	
	CC81-3KV-SL-020~151K a**	See specific specification				0.6	
	CC81-6KV-SL-020~101K a**	See specific specification				0.6	
							k 式 

■ Marking

	① Temperature Characteristic
	② Rated Capacitance
	③ Tolerance of Capacitance
	④ Rated Voltage

■ Structure



- ① Coating : Epoxy Resin
- ② Dielectric : Ceramic
- ③ Electrode : Silver or Copper
- ④ Solder : Alloy Tin
- ⑤ Lead wire : CP Lead



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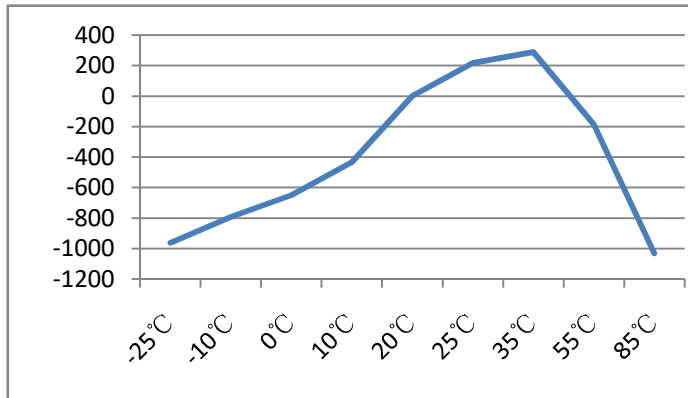
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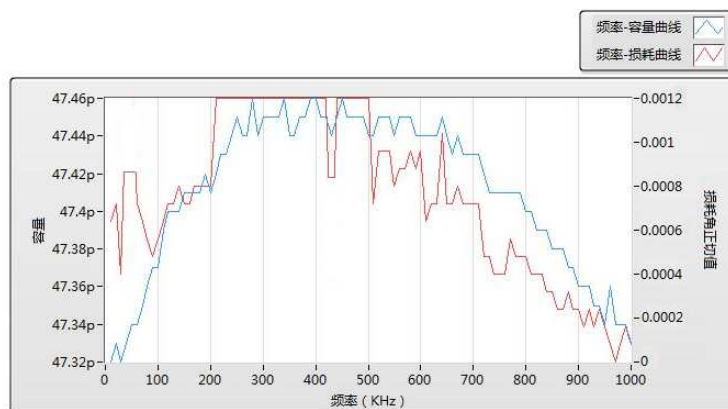
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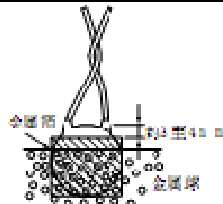
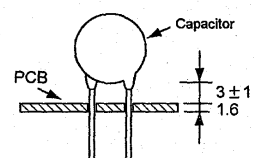
■ Temperature Characteristic Curve



■ Cap. & D.F.—Fre. Curve





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■ Specification and Test Method									
Item		Specifications		Test Method					
1. Storage Temp. Range		-40℃~+85℃							
2. Operating Temp. Range		-25℃~+125℃							
3. Appearance and dimension		Appearance has no marked defect Dimensions should be within specified tolerance		Appearance be watched on sight Dimension be measured by caliper					
4. Marking		Should be discerned easily		Be watched on sight					
5. Capacitance		Within specified tolerance		Temp. 20℃±2℃, Vol. 1.0±0.2V Freq. 1±0.2MHz,					
6. Dissipation Factor(D.F.)		0.15%max.							
7 Insulation Resistance(I.R.)		10000M Ω min.		The insulation resistance should be measured with 500±50VDC within 60±5sec of charging					
8 Dielectric Strength	Between Lead Wires	No failure		Apply a voltage of 1.5U _R +500VDC for 1 to 5 sec. between the lead Wires.(Charge/discharge current≤50mA) First, the terminals of the capacitor should be connected together. Then, as shown in figure at right, a metal foil body of the capacitor to the distance of about 3 to 4mm from each terminal. Then, the capacitor should be inserted into a container filled with metal balls of about 1mm diameter. Finally, apply a voltage of 1.3KV DC for 1 to 5 sec. Between the capacitor lead wires and metal balls. 					
	Body Insulation	No failure							
9. Temp. Char.				The capacitance should be measured at each step as below table.					
		Char.	Capacitance Change						
		SL	+350~-1000ppm/℃ (+20~+85℃)						
				Step	1	2	3	4	5
				Temp.(℃)	20±2	-25±2	20±2	85±2	20±2
10 Vibration Resistance	Appearance	No marked defect		The capacitor should be firmly soldered to the supporting lead wire and vibrated at a frequency range of 10 to 55Hz. 1.5mm in total amplitude. With about a 1 minute rate of vibration change from 10Hz to 55Hz and back to 10Hz.Apply for a total of 6 hrs. 2hrs .each in 3 mutually perpendicular directions. 					
	Capacitance Change	Within specified tolerance							
	Dissipation Factor(D.F.)/Q	0.2%max.							



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Item		Specifications		Test Method			
11. Strength of Lead wires		Tensile	Lead wire should not be cut off and capacitor should not be broken.		Fix the body of the capacitor and apply a tensile weight gradually to each lead wire in the radial direction of capacitor up to 10N,and keep for 10±1sec.		
		Bending			Each lead wire should be subjected to 5N weight and then a 90° bend, at the point of egress ,in one direction return to original position, and then a 90° bend in the opposite direction at the rate of one bend in 2 to 3s for 2 times.		
12. Solder ability		Lead wire should be soldered With uniformly coated on the axial direction over 90% of the circumferential direction		The lead wires of the capacitor should be dipped into alcohol Solution of 25% wt rosin and then into molten solder of 245°C within 2.0sec.In both case the depth of dipping is up to about 2.0 to 2.5mm from the root of the lead wires.			
13. Soldering Effect		Appearance	No marked defect		The lead wires should be immersed in solder of 260±10°C up to 1.5 to 2.0mm from the roof of terminal for 3+1/-0sec.		
		Capacitance Change	Within specified tolerance				
		I.R.	1000MΩ min				
		Dielectric Strength	Per Item 8.				
14. Humidity (under Steady State)		Appearance	No marked defect		Set the capacitor for 500+24/-0 hours at 40±2°C in 90-95% RH. Pre-treatment: The capacitor should be placed at 85±2°C for 1 hour, then, placed at room condition for 24±2 hours before initial measurement. Post-treatment: Capacitors should be stored for 24±2 hours at room condition.		
		Capacitance Change	Char.	Capacitance Change			
			SL	±5% or ±0.5pF (whichever is larger)			
		Dissipation Factor(D.F.)/Q	Char.	Specifications			
			SL	≤0.3%			
16. 寿命试验		I.R.	4000MΩ min		Apply a DC voltage of 1.5U _R for 1000h at 125°C (Charge/discharge current≤50mA) Pre-treatment: The capacitor should be placed at 85±2°C for 1 hour, then, placed at room condition for 24±2 hours before initial measurement. Post-treatment: Capacitors should be stored for 24±2 hours at room condition.		
		Dielectric Strength	Per Item 8.				
		Appearance	无显著异常				
		Capacitance Change	Char.	Capacitance Change			
			SL	±5% or ±0.5pF (whichever is larger)			
13. Soldering Effect		Dissipation Factor(D.F.)/Q	Char.	Specifications			
			SL	≤0.3%			
		I.R.	2000MΩ min				
		Dielectric Strength	参考项目 8				



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■ Package
Bulk
packing bag



Inner package

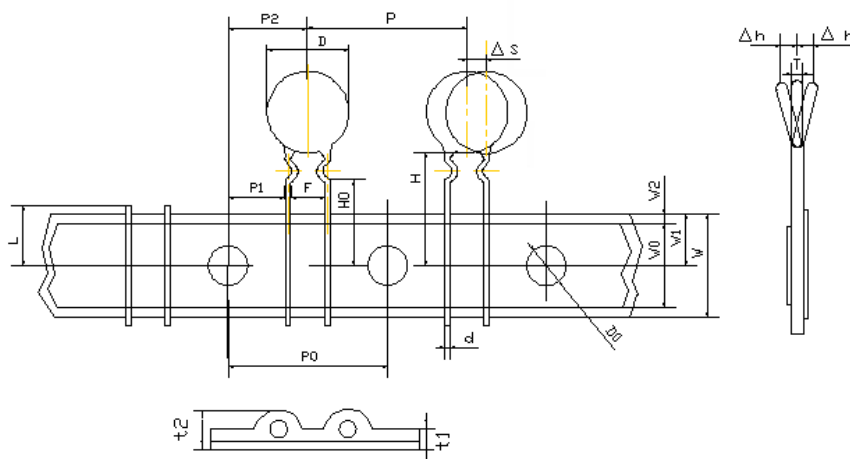


Dimension: mm			MOQ (Kpcs)	Inner package quantity (Kpcs)
L±10	W±5	H±5		
330	240	120	1 (短脚)	—
			0.5 (长脚)	—



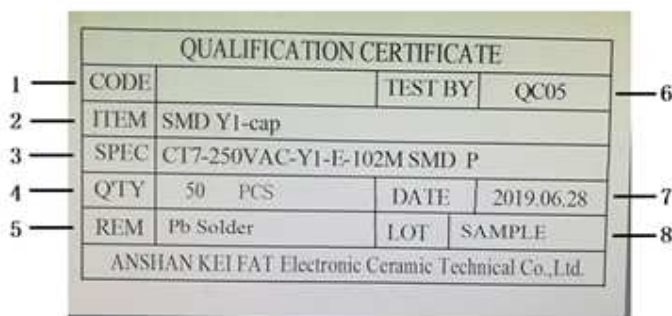
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Taping



符号	尺寸 (mm)	符号	尺寸 (mm)
P0	12.7 ± 0.2	W2	3.0max.
P	12.7 ± 1.0	t1	0.6 ± 0.3
F	5 ± 0.3	t2	1.5max.
P1	3.85 ± 0.5	D	7.0max.
P2	6.35 ± 1.0	D0	4 ± 0.1
H0	16.5 ± 0.5	d	0.55 ± 0.05
H	18.0 ± 1.0	L	11max.
W	18.0 ± 0.5	T	4.0max.
W0	10.0 ± 1.0	△S	0 ± 1.0
W1	$9.0^{+0}_{-0.5}$	△h	$\pm 0.8\text{max.}$

Product Label



No.	Description	No.	Description
1	Code Number	5	Remark
2	ITEM	6	Check
3	SPEC	7	Produce Date
4	Quantity	8	Batch



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■ **Caution (Rating)**

1. Operating Voltage

When DC-rated capacitors are to be used in AC or ripple current circuits, be sure to maintain the V p-p value of the applied voltage or the Vo-p that contains DC bias within the rated voltage range.

When the voltage is applied to the circuit, starting or stopping may generate irregular voltage for a transit period because of resonance or switching. Be sure to use a capacitor with a rated voltage range that includes these irregular voltages.

Voltage	DC Voltage	DC+AC Voltage	AC Voltage	Pulse Voltage (1)	Pulse Voltage (2)
Positional Measurement					

2. Operating Temperature and Self-generated Heat

Keep the surface temperature of a capacitor below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself. When the capacitor is used in a high-frequency current, pulse current or similar current, it may have self-generated heat due to dielectric loss. Applied voltage load should be such that self-generated heat is within 20°C under the condition where the capacitor is subjected to an atmospheric temperature of 25°C. When measuring, use a thermocouple of small thermal capacity-K of $\Phi 0.1\text{mm}$ under conditions where the capacitor is not affected by radiant heat from other components or wind from surroundings. Excessive heat may lead to deterioration of the capacitor's characteristics and reliability. (Never attempt to perform measurement with the cooling fan running. Otherwise, accurate measurement cannot be ensured.)

3. Test Condition for Withstanding Voltage

(1) Test Equipment

Test equipment for AC withstanding voltage should be used with the performance of the wave similar to 50/60Hz sine wave.

If the distorted sine wave or overload exceeding the specified voltage value is applied, a defect may be caused.

(2) Voltage Applied Method

When the withstanding voltage is applied, the capacitor's lead or terminal should be firmly connected to the output of the withstanding voltage test equipment, and then the voltage should be raised from near zero to the test voltage.



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<p>If the test voltage without the raise from near zero voltage would be applied directly to capacitor, test voltage should be applied with the zero cross.* At the end of the test time, the test voltage should be reduced to near zero, and then capacitor's lead or terminal should be taken off the output of the withstanding voltage test equipment. If the test voltage without the raise from near zero voltage would be applied directly to capacitor, the surge voltage may rise, and therefore, a defect may be caused.</p> <p>*ZERO CROSS is the point where voltage sine wave passes 0V. See the figure at below.</p> <div data-bbox="1050 707 1396 920"></div>		
<p>4. Fail-Safe</p> <p>When the capacitor is broken, failure may result in a short circuit. Be sure to provide an appropriate fail-safe function like a fuse on your product if failure could result in an electric shock, fire or fuming.</p> <p>FAILURE TO FOLLOW THE ABOVE CAUTIONS MAY RESULT, WORST CASE, IN A SHORT CIRCUIT AND CAUSE FUMING OR PARTIAL DISPERSION WHEN THE PRODUCT IS USED.</p> <p>Caution (Storage and Operating Condition)</p> <p>The insulating Epoxy molded capacitors does not form a perfect seal; therefore, do not use or store capacitors in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt or the like are present. And avoid exposure to moisture. Before cleaning, bonding or molding this product, verify that these processes do not affect produce quality by testing the performance of a cleaned, bonded or molded product in the intended equipment. Store the capacitors where the temperature and relative humidity do not exceed -40 to 85 degrees centigrade and 15 to 85%. Use capacitors within 6 months after delivery.</p> <p>FAILURE TO FOLLOW THE ABOVE CAUTIONS MAY RESULT, WORST CASE, IN A SHOCT CIRCUIT AND CAUSE FUMING OR PARTIAL DISPERSION WHEN THE PRODUCT IS USED.</p> <p>Caution (Soldering, Mounting and Handing)</p> <p>1. Vibration and Impact</p> <p>Do not expose a capacitor or its pins to excessive shock or vibration during use.</p> <p>Excessive shock or vibration may cause fatigue destruction of lead wires mounted on the circuit board.</p> <p>Please take measures to hold a capacitor on the circuit boards by adhesive, molding resin or another coating.</p> <p>Please confirm there is no influence of holding measures on the product with the intended equipment.</p>		



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<p>2. Soldering</p> <p>When soldering this product to a PCB/PWB, do not exceed the solder heat resistance specifications of the capacitor. Subjecting this product to excessive heating could melt the internal junction solder and may result in thermal shocks that can crack the ceramic element.</p> <p>Soldering the capacitor with a soldering iron should be performed in the following conditions.</p> <p>Temperature of iron-tip: 400 degrees C. max.</p> <p>Soldering iron wattage: 50W max.</p> <p>Soldering time: 3.5 sec. max.</p> <p>3. Bonding, Resin Molding and Coating</p> <p>Before bonding, molding or coating this product, verify that these processes do not affect the quality of capacitor by testing the performance of the bonded, molded or coated product in the intended equipment.</p> <p>In case the amount of applications, dryness/hardening conditions of adhesives and molding resins containing organic solvents (ethyl acetate, methyl ethyl ketone, toluene, etc.) are unsuitable, the outer coating resin of a capacitor is damaged by the organic solvents and it may result, worst case, in a short circuit.</p> <p>The variation in thickness of adhesive, molding resin or coating may cause outer coating resin cracking and/or ceramic element cracking of a capacitor in a temperature cycling.</p> <p>4. Treatment after Bonding, Resin Molding and Coating</p> <p>When the outer coating is hot (over 100 degrees C.) after soldering, it becomes soft and fragile. Therefore, please be careful not to give it mechanical stress.</p> <p>FAILURE TO FOLLOW THE ABOVE CAUTIONS MAY RESULT, WORST CASE, IN A SHOOT CIRCUIT AND CAUSE FUMING OR PARTIAL DISPERSION WHEN THE PRODUCT IS USED.</p> <p>Notice (Soldering and Mounting)</p> <p>Cleaning (ultrasonic cleaning)</p> <p>To perform ultrasonic cleaning, observe the following conditions.</p> <p>Rinse bath capacity: Output of 20 watts per liter or less. Rinsing time: 5 min. max.</p> <p>Do not vibrate the PCB/PWB directly. Excessive ultrasonic cleaning may lead to fatigue destruction of the pins.</p> <p>Notice (Rating)</p> <p>1. Capacitance Change of Capacitors</p> <p>(1) For CH/SL/DL char.</p> <p>Capacitance might change a little depending on a surrounding temperature or an applied voltage.</p> <p>Please contact us if you use a strict constant time circuit.</p>		



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<p>2. Performance Check by Equipment</p> <p>Before using a capacitor, check that there is no problem in the equipment's performance and the specifications.</p> <p>Generally speaking, CLASS 2 (B/E char.) ceramic capacitors have voltage dependence characteristics and temperature dependence characteristics in capacitance, so the capacitance value may change depending on the operating condition in the equipment. Therefore, be sure to confirm the apparatus performance of receiving influence in the capacitance value change of a capacitor, such as leakage current and noise suppression characteristic.</p> <p>Moreover, check the surge-proof ability of a capacitor in the equipment, if needed, because the surge voltage may exceed specific value by the inductance of the circuit.</p>		