N	Manufacturer : Anshan Ke	ifat Electronic Ceramic Te	chnical Co.,Ltd. No	:
	A1	Class 4 for D	d	<b>C</b>
	Approvai	Sheet for Pi	oauct Speci	ilication
_	7			
(	Customer:			
_	T . 14	T C C	• •,	e N / 1 1 1
F	Product: Lead type	Low frequency C	eramic capacitors	of Medium and
h	nigh voltage			
F	PART No.:			
N	Afr. P/N:			
Ι	Date: 年 月	日		
	Manuf	acturer	Customer	Confirm
	Prepared by	薛志豪	合格 OK □ 不合格 NG □	-

Manufacturer		Customer	Confirm
Prepared by	薛志豪	合格 OK □ 不合格 NG □	
Checked by	于金龙	Checked by	
Approved by	范垂旭	Approved by	

No. 177 Xingsheng Road Tiexi District Anshan, China Address:

Fax: 86-412-8200366 Tel.: 86-412-8234566

E-mail: asaec111@126.com



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		Revision History		
Edition	Date	Contents of formulation / modification /	Formulation	Approval
	2 3.33	repeal		, <b>, , , , , , , , , , , , , , , , , , </b>
Α		New edition released	薛志豪	于金龙
		I I		



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#### ■ Features

- High breakdown strength
- Operating temperature range -25~+125 °C
- Coated with flame-retardant epoxy resin (conforming to UL94V-0 standard).
- DC1KV~6KV Rated Voltage item are available.
- Bulk or taping product can be available.

# ■ Application

Using for H-out and supply circuits of color TV and monitor, copy machine, inverter lighting.

# ■ Part Number Designation

CT81 - 1KV - B - 221 K k 5 T

① ② ③ ④ ⑤ ⑥ ⑦ ⑧

①Type

②Rated	Voltage
--------	---------

Code	Type Designation
CT81	Class II High-Voltage

# **③Temperature Characteristic**

GB	EIA	Temp. range	Cap. Change
В	Y5P	<b>-25</b> ∼+85℃	±10%
Е	Y5U	<b>-25</b> ∼+85℃	<b>-56%∼+22%</b>
F	Y5V	-25∼+85°C	-82%∼+22%

## **5**Tolerance

Code	Tolerance
К	±10%
М	$\pm$ 20%
Z	<b>−20%∼+80%</b>

# **7**Lead Space

Code	Lead Space
5	$5.0\pm0.5$ mm
7.5	7.5 $\pm$ 0.5mm

Code	Rated Vol. (DC)
1KV	1000V
3KV	3000V

## **4** Capacitance

Code	Capacitance
221	220 pF
222	2200 pF

## **6**Lead Shape

Code	Shape
а	In-kink
k	Out-kink

## ®Package

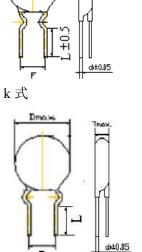
Code	Shape
Т	Taping



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## Appearance and Structure

- 44							
CODE	CODE NO.	$D_{max}$	T <sub>max</sub>	L	F	d	STYLE
		(mm)	(mm)	(mm)	(mm)	(mm)	
	CT81-1KV-B-470~103K k**	See spe	cific sp	ecific	ation	0.5	a式
	CT81-1KV-E-471~103M k**	See spec	cific sp	ecific	ation	0.5	Dmo.x Tmox.
	CT81-1KV-F-102~223M k**	See spe	cific sp	ecific	ation	0.5	[(   )
	CT81-2KV-B-470~332K k**	See spee	cific sp	ecific	ation	0.6	
	CT81-2KV-E-471~103M k**	See spec	cific sp	ecific	ation	0.6	<u> </u>
	CT81-2KV-F-102~103M k**	See spee	cific sp	ecific	ation	0.6	F dı±0.I
	CT81-3KV-B-470~222K k**	See spec	cific sp	ecific	ation	0.6	k 式
	CT81-3KV-E-471~682M k**	See spee	cific sp	ecific	ation	0.6	Dmax Tno
	CT81-3KV-F-102~103M k**	See spec	cific sp	ecific	ation	0.6	
	CT81-6KV-B-470~152K k**	See spec	cific sp	ecific	ation	0.6	
	CT81-6KV-E-471~472M k**	See spec	cific sp	ecific	ation	0.6	
	CT81-6KV-F-102~103M k**	See spec	cific sp	ecific	ation	0.6	
							32

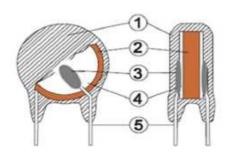


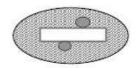
# Marking



- ① Temperature Characteristic
- 2 Rated Capacitance
- 3 Tolerance of Capacitance
- 4 Rated Voltage

#### Structure





①Coating: Epoxy Resin ②Dielectric : Ceramic

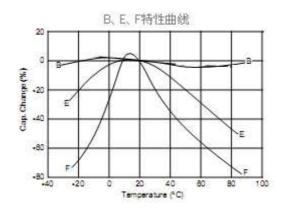
③Electrode: Silver or Copper

**4**Solder : Alloy Tin ⑤Lead wire: CP Lead

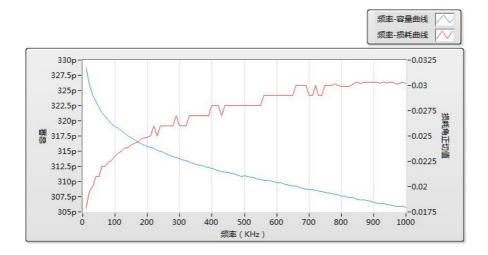


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# ■ Temperature Characteristic Curve



# ■ Cap. & D.F.—Fre. Curve





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# ■ Specification and Test Method

It	tem	Specifications			Test Method			
1. Storage Temp. Range		−40°C∼+85°C						
2. Operating Temp. Range		−25°C∼+12	!5°C					
3 . Appearance and		Appearance	has no marked defec	t	Appearance be watched on sight			
dimension			s should be within		Dimension be measured by caliper			
4 Morks	in ~	specified to	iscerned easily		Be watched on sight			
4. Mark			ified tolerance		Temp. 20°C±2°C,			
	citance				Vol. 1.0±0.2V			
b . Dissipat	tion Factor(D.F.)	Char.	Specifications		Freq. 1±0.2KHz,			
		В	≤2.5%					
		E, F	≪3.5%					
7 Insulation	Resistance(I.R.)	10000M Ω I	min.		The insulation resistance should be measured with			
					500±50VDC within 60±5sec of charging			
8	Between Lead	No failure			Apply a voltage of 1.5U <sub>R</sub> +500V DC for 1 to 5 sec. between the lead Wires.(Charge/discharge current≤50mA)			
Dielectric	Wires							
Strength	Body	No failure			First, the terminals of the capacitor should be connected			
	Insulation				together. Then, as shown in			
					figure at right, a metal foil body			
					of the capacitor to the distance of about 3 to 4mm from each			
					terminal. Then, the capacitor			
					should be inserted into a container filled with metal balls of about 1mm diameter.			
					Finally, apply a voltage of 1.3KV DC for 1 to 5 sec. Between			
					the capacitor lead wires and metal balls.			
9. Temp.	Char.	Char.	Canasitanas C	h a m = a	The capacitance should be measured at each step as below			
		B	i s		table.			
		E	± 10% −56%~+22%		Step 1 2 3 4 5			
		F	-82%~+22°		Temp.(°C) 20±2 -25±2 20±2 85±2 20±2			
10	Annogranas			· u	The conseiter should be			
10	Appearance	No marked			The capacitor should be			
Vibration	Capacitance Change	Within specified tolerance			firmly soldered to the PCB			
Resistanc	Onlange	Char.	Specifications		supporting lead wire and			
е	D:	B	≤2.5%		vibrated at a frequency range			
	Dissipation	E\ F			of 10 to 55Hz. 1.5mm in total			
	Factor(D.F.)/	E > F	~3.3/₀		amplitude. With about a 1 minute rate of vibration change			
	Q				from 10Hz to 55Hz and back to 10Hz.Apply for a total of 6			
					hrs. 2hrs .each in 3 mutually perpendicular directions.			



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Ite	m	Sı	pecifications		Test Method		
11. Strength of Lead wires	Tensile	Lead wire should not be cut off and capacitor should not be broken.		Fix the body of the capacitor and apply a tensile weight gradually to each lead wire in the radial direction of capacitor up to 10N,and keep for 10±1sec.  Each lead wire should be subjected to 5N weight and			
	Bending			90° bend, at the p original position, a direction at the rat	oint of egress ,in one and then a 90° ben e of one bend in 2 to	direction return to d in the opposite 3s for 2 times.	
12. Solder ab	ility	uniformly	should be soldered With coated on the axial ver 90% of the ntial direction	The lead wives of the conseitor about he dispendints			
	Appearance	No marked	defect	The lead wires shou	uld be immersed The	Capacitor	
13. Soldering	Capacitance Change	Within specified tolerance			n solder of 260±10°C up to 1.5 to		
Effect	I.R.		1000M Ω min	for 3+1/-0sec.			
	Dielectric Strength		Per Item 8.				
	Appearance	No marked defect					
14. Humidity	Capacitance Change	Char. B E、F	Capacitance Change ±10% ±20%	90-95% RH.		hours at 40±2°C in	
(under Steady State)	Dissipation Factor(D.F.)/Q	Char. B E. F	Specifications ≤5.0% ≤7.0%	for 1 hour, then, pl before initial measu Post-treatment: C	apacitors should be	on for 24±2 hours	
	I.R.	4	4000MΩ min	Tiours at room con	hours at room condition.		
	Dielectric Strength		Per Item 8.				
	Appearance	无显著异常	ਰੰ ਹ		of 1.5U <sub>R</sub> for 1000h a	at 125°C	
	Capacitance Change	Char. B E. F	Capacitance Change ±10% ±20%	(Charge/discharge current≤50mA) Pre-treatment: The capacitor should be placed for 1 hour, then, placed at room condition for before initial measurement.  Post-treatment: Capacitors should be stored		on for 24±2 hours	
16. 寿命试验	Dissipation Factor(D.F.)/Q	Char. B E、F	Specifications ≤5.0% ≤7.0%	hours at room cond	dition.		
	I.R.		2000MΩ min				
	Dielectric Strength		Per Item 8.				

Strength



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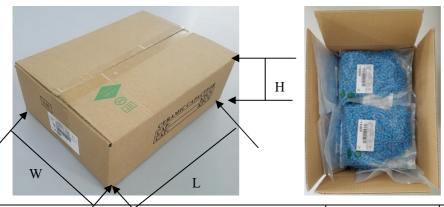
# ■ Package

Bulk

# packing bag



# Inner package

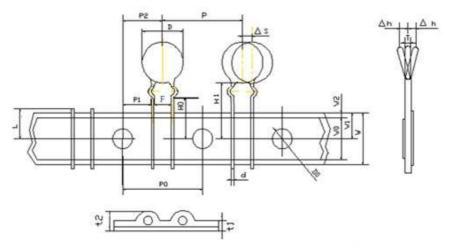


/	Dimension: mm		MOQ	Inner package quantity
L±10	W±5	H±5	(Kpcs)	(Kpcs)
330	240	120	1 (短脚)	
			0.5(长脚)	



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# **Taping**



符号	尺寸(mm)	符号	尺寸(mm)
P0	12.7 $\pm$ 0.2	W2	3.0max.
P	12.7 $\pm$ 1.0	t1	$0.6 \pm 0.3$
F	$5.0\pm0.5$	t2	1.5max.
P1	$3.85\pm0.5$	D	13.5max.
P2	$6.35\pm1.0$	DO	$4\pm 0.1$
НО	16. $5\pm 0.5$	d	$0.6 \pm 0.05$
Н	$18.0\pm 1.0$	L	11max.
W	$18.0\pm0.5$	T	4.5max.
WO	$10.0\pm 1.0$	ΔS	$0 \pm 0.8$
W1	9.0+0	∆h	$\pm 1.0$ max.

# **Product Label**



No.	Description	No.	Description
1	Code Number	5	Remark
2	ITEM	6	Check
3	SPEC	7	Produce Date
4	Quantity	8	Batch



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## ■ Caution (Rating)

#### 1. Operating Voltage

When DC-rated capacitors are to be used in AC or ripple current circuits, be sure to maintain the V p-p value of the applied voltage or the Vo-p that contains DC bias within the rated voltage range.

When the voltage is applied to the circuit, starting or stopping may generate irregular voltage for a transit period because of resonance or switching. Be sure to use a capacitor with a rated voltage range that includes these irregular voltages.

Voltage	DC Voltage	DC+AC Voltage	AC Voltage	Pulse Voltage (1)	Pulse Voltage (2)
Positional Measurement	Vu-p	Vu-p	Vp-p	V <sub>D</sub> -p	Vp-p

#### 2. Operating Temperature and Self-generated Heat

Keep the surface temperature of a capacitor below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself. When the capacitor is used in a high-frequency current, pulse current or similar current, it may have self-generated heat due to dielectric loss. Applied voltage load should be such that self-generated heat is within  $20^{\circ}\text{C}$  under the condition where the capacitor is subjected to an atmospheric temperature of  $25^{\circ}\text{C}$ . When measuring, use a thermocouple of small thermal capacity-K of  $\Phi$ 0.1mm under conditions where the capacitor is not affected by radiant heat from other components or wind from surroundings. Excessive heat may lead to deterioration of the capacitor's characteristics and reliability. (Never attempt to perform measurement with the cooling fan running. Otherwise, accurate measurement cannot be ensured.)

#### 3. Test Condition for Withstanding Voltage

#### (1) Test Equipment

Test equipment for AC withstanding voltage should be used with the performance of the wave similar to 50/60Hz sine wave.

If the distorted sine wave or overload exceeding the specified voltage value is applied, a defect may be caused.

## (2) Voltage Applied Method

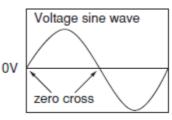
When the withstanding voltage is applied, the capacitor's lead or terminal should be firmly connected to the output of the withstanding voltage test equipment, and then the voltage should be raised from near zero to the test voltage.



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If the test voltage without the raise from near zero voltage would be applied directly to capacitor, test voltage should be applied with the zero cross.\* At the end of the test time, the test voltage should be reduced to near zero, and then capacitor's lead or terminal should be taken off the output of the withstanding voltage test equipment. If the test voltage without the raise from near zero voltage would be applied directly to capacitor, the surge voltage may rise, and therefore, a defect may be caused.

\*ZERO CROSS is the point where voltage sine wave passes 0V. See the figure at below.



#### 4. Fail-Safe

When the capacitor is broken, failure may result in a short circuit. Be sure to provide an appropriate fail—safe function like a fuse on your product if failure could result in an electric shock, fire or fuming.

FAILURE TO FOLLOW THE ABOVE CAUTIONS MAY RESULT, WORST CASE, IN A SHORT CIRCUIT AND CAUSE FUMING OR PARTIAL DISPERSION WHEN THE PRODUCT IS USED.

#### Caution (Storage and Operating Condition)

The insulating Epoxy molded capacitors does not form a perfect seal; therefore, do not use or store capacitors in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt or the like are present. And avoid exposure to moisture. Before cleaning, bonding or molding this product, verify that these processes do not affect produce quality by testing the performance of a cleaned, bonded or molded product in the intended equipment. Store the capacitors where the temperature and relative humidity do not exceed -40 to 85 degrees centigrade and 15 to 85%. Use capacitors within 6 months after delivery.

FAILURE TO FOLLOW THE ABOVE CAUTIONS MAY RESULT, WORST CASE, IN A SHOCT CIRCUIT AND CAUSE FUMING OR PARTIAL DISPERSION WHEN THE PRODUCT IS USED.

#### Caution (Soldering, Mounting and Handing)

#### 1. Vibration and Impact

Do not expose a capacitor or its pins to excessive shock or vibration during use.

Excessive shock or vibration may cause fatigue destruction of lead wires mounted on the circuit board.

Please take measures to hold a capacitor on the circuit boards by adhesive, molding resin or another coating.

Please confirm there is no influence of holding measures on the product with the intended equipment.



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#### 2. Soldering

When soldering this product to a PCB/PWB, do not exceed the solder heat resistance specifications of the capacitor. Subjecting this product to excessive heating could melt the internal junction solder and may result in thermal shocks that can crack the ceramic element.

Soldering the capacitor with a soldering iron should be performed in the following conditions.

Temperature of iron-tip: 400 degrees C. max.

Soldering iron wattage: 50W max.

Soldering time: 3.5 sec. max.

#### 3. Bonding, Resin Molding and Coating

Before bonding, molding or coating this product, verify that these processes do not affect the quality of capacitor by testing the performance of the bonded, molded or coated product in the intended equipment.

In case the amount of applications, dryness/hardening conditions of adhesives and molding resins containing organic solvents (ethyl acetate, methyl ethyl ketone, toluene, etc.) are unsuitable, the outer coating resin of a capacitor is damaged by the organic solvents and it may result, worst case, in a short circuit.

The variation in thickness of adhesive, molding resin or coating may cause outer coating resin cracking and/or ceramic element cracking of a capacitor in a temperature cycling.

#### 4. Treatment after Bonding, Resin Molding and Coating

When the outer coating is hot (over 100 degrees C.) after soldering, it becomes soft and fragile. Therefore, please be careful not to give it mechanical stress.

FAILURE TO FOLLOW THE ABOVE CAUTIONS MAY RESULT, WORST CASE, IN A SHOCT CIRCUIT AND CAUSE FUMING OR PARTIAL DISPERSION WHEN THE PRODUCT IS USED.

#### Notice (Soldering and Mounting)

Cleaning (ultrasonic cleaning)

To perform ultrasonic cleaning, observe the following conditions.

Rinse bath capacity: Output of 20 watts per liter or less. Rinsing time: 5 min. max.

Do not vibrate the PCB/PWB directly. Excessive ultrasonic cleaning may lead to fatigue destruction of the pins.

## Notice (Rating)

- 1. Capacitance Change of Capacitors
- (1) For B/E /F char.

Capacitors have an aging characteristic, whereby the capacitor continually decreases its capacitance slightly if the capacitor is left on for a long time. Moreover, capacitance might change greatly depending on the surrounding temperature or an applied voltage. Therefore, it is not likely to be suitable for use in a constant time circuit.



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2. Performance Check by Equipment				
Before using a capacitor, check that there is no problem in the equipment's p	erformance and th	e specifications.		
Generally speaking, CLASS 2 (B/E char.) ceramic capacitors have voltage dependence characteristics and temperature				
dependence characteristics in capacitance, so the capacitance value may cha	nge depending on t	he operating condition		
in the equipment. Therefore, be sure to confirm the apparatus performance	of receiving influer	nce in the capacitance		
value change of a capacitor, such as leakage current and noise suppression of	haracteristic.			
Moreover, check the surge-proof ability of a capacitor in the equipment, if	needed, because t	he surge voltage may		
exceed specific value by the inductance of the circuit.				