	Mo	nufact	Iror	Cus	stomer Confirm
Date:	年	月	日		
Mfr. P/N:					
PART No.:					
Product: Sa	fety C	Certifi	cated Ceramic	Capacitor Chip	
Customer:					
App	rov	al S	heet for]	Product S _J	pecification
Manufacturer:	Anshan	Keifat	Electronic Ceramic	Technical Co.,Ltd.	No:

Manufacturer		Customer	Confirm
Prepared by	刘春鹏	合格 OK □ 不合格 NG □	
Checked by	范亚岩	Checked by	
Approved by	范垂旭	Approved by	

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	Anshan Keifat Electronic Ceramic Technical Co., Ltd.			
			PART NO.	
C - £ -			Edition	Page
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		Revision History		
Edition	Date	Contents of formulation / modification / repeal	Formulation	Approval
Α		New edition released	刘春鹏	范亚岩



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■ Trait

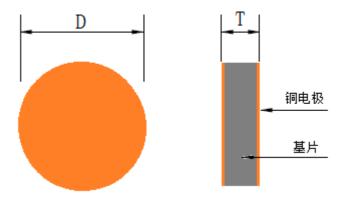
- The copper electrode is prepared by chemical deposition method.
- The edge of the chip is polished by an edge grinding process.
- Symmetrical full electrodes, more uniform electric field, eliminates the flexible bending vibration caused by electro-induced strain, and improves the dielectric strength of the ceramic capacitor.

■ Application

- Used as an AC line filter and switching power supply and AC converter primary coupling X/Y grade capacitor chip.
 - Range Of Capacity

22 pF to 10,000pF

Dimensions



D:Chip diameter

T:Chip thickness

■ Shape deviation:

 $D=\pm 0.1 \text{mm}$ $T=\pm 0.05 \text{mm}$

■ Substrate abbreviation:

Five digits of substrate number

The first two digits represent the chip diameter in 1/10 mm, and the last three digits represent the chip thickness in 1/100 mm.

Six digits of the substrate number

The first three digits indicate the diameter of the chip, in 1/10 mm, and the last three digits indicate the thickness of the chip in 1/100 mm.



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Specifications

Rated Voltage Temp. char.		Cap. range	D (mm)	T (mm)
	SL	22~100pF	See specific s	pecification
250VAC/400VAC-Y1	B (Y5P)	47~1500pF	See specific s	pecification
250 VAC/400 VAC-11	E (Y5U, Z5U)	470~4700pF	See specific specification	
	F (Y5V, Z5V)	1000~10000pF	See specific s	pecification
	SL	22~150pF	See specific s	pecification
250VAC-Y2	B (Y5P)	47~2200pF	See specific s	pecification
250VAC-12	E (Y5U, Z5U)	470~1000pF	See specific s	pecification
	F (Y5V, Z5V)	1000~10000pF	See specific s	pecification

■ Room Condition

Temp.: 15°C~35°C Humi.: 45%~75%RH

Pres.: 86~106kPa (860~1060mbar)

■ Test Condition

Temp.: $20\pm2^{\circ}$ R.H.: $60\sim70\%$ RH

Vol: 1.0 ± 0.2 Vrms Freq: Y5P, Y5U, Y5V, Z5V, Z5U: 1 ± 0.1 KHz; SL: 1 ± 0.1 MHz

■ Temperature Characteristics

Category temperature range	Capacity change rate	GB code	EIAcode
-25℃~+85℃	±10%	2B4	Y5P
-25℃~+85℃	+22~-56%	2E4	Y5U
-25℃~+85℃	+30~-82%	2F4	Y5V
+10℃~+85℃	+22~-56%		Z5U
+10℃~+85℃	+30~-82%		Z5V
+25℃~+85℃	+350~-1000ppm/℃		SL
-25°C∼+85°C	-750±120ppm/℃	UJ	N750



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■ Part Code Designation

CT7 -400VAC -Y1 - B- 221 k -47 180 ① ② ③ ④ ⑤ ⑥ ⑦ ⑧

1Type

Code	Туре
CT7	II High—Voltage

③Grade number

Code	Grade number
Y1	Y1
Y2	Y2

⑤Capacitance

Code	Capacitance
47	47pF
101	100 pF
102	1000 pF

7 Chip diameter

Code	unit	
47	47/10 mm	

■ Withstand voltage test:

test temperature: 20——25℃ Test condition: Silicone oil

Pressure test: $Y1 \ge 6.5 \text{KVAC}$ $Y2 \ge 4.0 \text{KVAC}$

②Rated Voltage

Code	Rated Vol.
400VAC	400V

4Temperature Characteristic

GB	EIA	Temp. range	Cap. Change	
В	Y5P	-25∼+85℃	±10%	
_	Y5U	-25 ∼+85℃	+22%~-56%	
E	Z5U	+10∼+85℃	+22%~-56%	
F	Y5V	-25 ∼+85℃	+30%~-82%	
	Z5V	+10∼+85℃	+30%~-82%	
5	SL	+25∼+85℃	+350∼-1000ppm/°C	
UJ	N750	-25∼+85℃	-750±120ppm/℃	

®Tolerance

Code	Tolerance	
J	± 5%	
K	±10%	
М	±20%	

8 Chip thickness

Code	unit	
180	180/100 mm	



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Storage and use period requirements

Storage environment requirements:

Temperature: -10°C[~] +40°C

Relative humidity: no more than 75%RH

Use period:

1. Without opening, the storage period is 6 months.

2. If it has been opened, use it up within 48 hours.

■ Packaging, storage and use conditions

Use vacuum packaging to prevent impact during transportation. Do not store the chip in corrosive gas, especially where chlorine, sulfur, acid, alkali, salt, etc.

It is strictly forbidden to touch the capacitor chip directly with your hands to prevent oxidation and pollution of the chip.

Used up as soon as possible after opening the bag during the production process, and keep the remaining chips in a sealed bag as far as possible.